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PTO/SB/21 (02-04)

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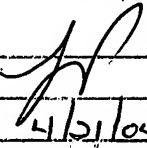
**TRANSMITTAL  
FORM**

(to be used for all correspondence after initial filing)

Application Number	10/692,091		
Filing Date	October 23, 2003		
First Named Inventor	Hung, Eric		
Art Unit	2186		
Examiner Name	N/A		
Total Number of Pages in This Submission	4	Attorney Docket Number	03-1236

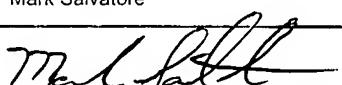
ENCLOSURES (Check all that apply)		
<input checked="" type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment/Reply <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input checked="" type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/ Incomplete Application <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation <input type="checkbox"/> Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s) _____	<input type="checkbox"/> After Allowance communication to Technology Center (TC) <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Other Enclosure(s) (please identify below): <ul style="list-style-type: none"> <li>1. Return Postcard</li> <li>2. Copy of References</li> </ul>
Remarks		

**SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT**

Firm or Individual name	Leo J. Peters, Reg. No. 33,562	
Signature		
Date	4/21/04	

**CERTIFICATE OF TRANSMISSION/MAILING**

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below.

Typed or printed name	Mark Salvatore	
Signature		Date 4-21-04

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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# BEST AVAILABLE COPY

PTO/SB/17 (10-03)

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## FEE TRANSMITTAL for FY 2004

Effective 10/01/2003. Patent fees are subject to annual revision.

Applicant claims small entity status. See 37 CFR 1.27

**TOTAL AMOUNT OF PAYMENT** (\$ 0.00)

### Complete if Known

Application Number	10/692,091
Filing Date	October 23, 2003
First Named Inventor	Hung, Eric
Examiner Name	N/A
Art Unit	2186
Attorney Docket No.	03-1236

### METHOD OF PAYMENT (check all that apply)

Check  Credit card  Money Order  Other  None

Deposit Account:

Deposit Account Number	12-2252
Deposit Account Name	LSI Logic Corporation

The Director is authorized to: (check all that apply)

- Charge fee(s) indicated below  Credit any overpayments  
 Charge any additional fee(s) or any underpayment of fee(s)  
 Charge fee(s) indicated below, except for the filing fee to the above-identified deposit account.

### FEE CALCULATION

#### 1. BASIC FILING FEE

Large Entity	Small Entity	Fee Description	Fee Paid
Fee Code (\$)	Fee Code (\$)		
1001 770	2001 385	Utility filing fee	
1002 340	2002 170	Design filing fee	
1003 530	2003 265	Plant filing fee	
1004 770	2004 385	Reissue filing fee	
1005 160	2005 80	Provisional filing fee	
<b>SUBTOTAL (1)</b>		(\$ 0.00)	

#### 2. EXTRA CLAIM FEES FOR UTILITY AND REISSUE

Total Claims	Extra Claims	Fee from below	Fee Paid
	-20**	=	
Independent Claims		X	
Multiple Dependent	- 3**	=	
		=	

Large Entity	Small Entity	Fee Description
Fee Code (\$)	Fee Code (\$)	
1202 18	2202 9	Claims in excess of 20
1201 86	2201 43	Independent claims in excess of 3
1203 290	2203 145	Multiple dependent claim, if not paid
1204 86	2204 43	** Reissue independent claims over original patent
1205 18	2205 9	** Reissue claims in excess of 20 and over original patent
<b>SUBTOTAL (2)</b>		(\$ 0.00)

\*\*or number previously paid, if greater; For Reissues, see above

### 3. ADDITIONAL FEES

Large Entity Small Entity

Fee Code (\$)	Fee Code (\$)	Fee Description	Fee Paid
1051 130	2051 65	Surcharge - late filing fee or oath	
1052 50	2052 25	Surcharge - late provisional filing fee or cover sheet	
1053 130	1053 130	Non-English specification	
1812 2,520	1812 2,520	For filing a request for ex parte reexamination	
1804 920*	1804 920*	Requesting publication of SIR prior to Examiner action	
1805 1,840*	1805 1,840*	Requesting publication of SIR after Examiner action	
1251 110	2251 55	Extension for reply within first month	
1252 420	2252 210	Extension for reply within second month	
1253 950	2253 475	Extension for reply within third month	
1254 1,480	2254 740	Extension for reply within fourth month	
1255 2,010	2255 1,005	Extension for reply within fifth month	
1401 330	2401 165	Notice of Appeal	
1402 330	2402 165	Filing a brief in support of an appeal	
1403 290	2403 145	Request for oral hearing	
1451 1,510	1451 1,510	Petition to institute a public use proceeding	
1452 110	2452 55	Petition to revive - unavoidable	
1453 1,330	2453 665	Petition to revive - unintentional	
1501 1,330	2501 665	Utility issue fee (or reissue)	
1502 480	2502 240	Design issue fee	
1503 640	2503 320	Plant issue fee	
1460 130	1460 130	Petitions to the Commissioner	
1807 50	1807 50	Processing fee under 37 CFR 1.17(q)	
1806 180	1806 180	Submission of Information Disclosure Stmt	
8021 40	8021 40	Recording each patent assignment per property (times number of properties)	
1809 770	2809 385	Filing a submission after final rejection (37 CFR 1.129(a))	
1810 770	2810 385	For each additional invention to be examined (37 CFR 1.129(b))	
1801 770	2801 385	Request for Continued Examination (RCE)	
1802 900	1802 900	Request for expedited examination of a design application	

Other fee (specify)

\*Reduced by Basic Filing Fee Paid

**SUBTOTAL (3)** (\$ 0.00)

### SUBMITTED BY

Name (Print/Type)	Leo J. Peters	Registration No. (Attorney/Agent)	33,562	Telephone	408-433-4578
Signature				Date	4/21/04

(Complete if applicable)

WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.

This collection of information is required by 37 CFR 1.17 and 1.27. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:**

Eric Hung et. al.

**Serial No. :** 10/692,091

**Filed :** October 23, 2003

For : Method Module Having Mirrored Placement of DRAM Integrated Circuits Upon for a Four-Layer Printed Circuit Board



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**Group Art Unit:** 2186

Examiner:

**Atty Docket :** 5201-27500 / 03-  
1236

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Mark Salvatore

April 21, 2004

Date \_\_\_\_\_

**Signature**

# INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached form, copies of which are attached, may be material to examination of above-identified application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR 1.56 and 1.97.

It is requested that the information disclosed herein be made of record in the application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

If it is determined that any additional fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 12-2252.

LSI Logic Corporation  
1621 Barber Lane, MS D-106  
Milipitas, CA 95035  
408-433-7475

Date: 4/21/02

Respectfully submitted,



Leo Peters

Reg. No. 33.562



APR 26 2004

JCA

PATENT &amp; TRADEMARK OFFICE

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT****Complete if Known**

Application Number	10/692,091
Filing Date	October-23, 2003
First Named Inventor	Eric Hung
Group Art Unit	2186
Examiner Name	
Attorney Docket No.	5201-27500 / 03-1236

Sheet 1

of 1

**U.S. PATENT DOCUMENTS**

Examiner Initials	Cite No.	U.S. Patent Document Number	Kind Code (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		6,618,320		Hasegawa et al.	09-09-2003	
		6,154,419		Shakkarwar	11-28-2000	
		6,615,326		Lin	09-02-2003	
		6,044,032		Li	03-28-2000	
		6,545,895		Li et al.	04-08-2003	

**OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS**

Examiner Initials	Cite No.	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where	T
		"Stub Series Terminated Logic for 2.5 Volts (SSTL 2)", JEDEC Standard, JESD8-9B, May 2002	
		"184 Pin PC 1600/2100 DDR SDRAM Unbuffered DIMM Design Specification, JEDEC Standard, Revision 1.1, April 2003	
		"TN-46-05, General DDR SDRAM Functionality", Micron Technology, July 2001	

Examiner signature		Date considered	
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Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO : Assistant Commissioner for Patents, Washington, DC 20231.